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SAI RAM INSTITUTE OF TECHNOLOGY

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Name : K.SUMATHI

Designation : AP Grade-II

Email : sumathi.ece@sairamit.edu.in

Qualification : M.E.,(Ph.D)

Specialization : APPLIED ELECTRONICS

Research Interest : RF TRANSMITTER DESIGN USING CMOS TECHNOLOGY

Experience in years : **Teaching** UG PG **Industry**

No. of Workshop/Conferences/
FDP attended : Workshop Conferences FDP

No. of Workshop/Conferences/
FDP Organized : Workshop Conferences FDP



Professional Membership	ISTE
Publications	National : Nil
	International: 03
	Book :Nil
Research Funded Projects	-
Patents	-

Achievements	-
Any Other Information	

List of Publications:-

1. Published a paper “**Performance study of 4:1 Multiplexer CMOS Logic Structures** “ during Feb 11th to 13th 2016 held at University college of Engineering(ICRTCCM'2016),Tindivanam.

2. Published a paper “**Dust Notes For Border Survelilance** “on March 10th 2017 held at ICMAET,Salem Tamilnadu.

3. Published a paper “**Study and Design of RF CMOS Oscillator** “in Asian Journal of Applied Science.

Workshop / Conferences / FDP Attended:-

1. Attended one day **workshop** on “**Computer Networking** ”, in Anna University , on 14/10/2006.

2. Attended one day **workshop** on “**Virtual Instrumentation** ”,conducted by National Instruments(India) Bangalore held on 25th September 2004 at Shree Motilal Kanhaiyalal Fomra Institute of Technology.

3. Attended one day **workshop** on “**A Refresher course on VLSI Technologies**”, held on 3rd September 2005 at Sakthi Mariamman Engineering College

4. Attended National level one day **workshop** on “**Embedded System Design**”, held on 2nd March 2012 at St.Joseph’s College of Engineering.

5. Attended National level Short-Term Training Program on “**MSP430 launchepad Based Embedded System Design**”, held on 27th September 2014 at VIT University.

6. Attended two days **workshop** titled ”**Hands on TRAIing in NS2**”, held on 6th & 7th November 2014 at Dhanalakshmi College of Engineering.

7. Attended National level one day **workshop** on “**How to write Research Papers for Referred Journals**”, held on 24th January 2015 at Sri Sairam Insititute of Technology.

8. Attended National level one day **workshop** on “**Brain SENSE**”, held on 28th March 2015 organized by the School of Electronics Engineering ,VIT University.
9. Attended one day **workshop** titled “**Research Methodology**”, held on 18th July 2015 at Sathyabama University.
10. Attended two days **workshop** titled “**Circuit Simulation Using EDA tools**” conducted by Live wire held on 4th & 5th August 2015 at Sri Sairam Insititute of Technology.
11. Attended two days Hands-on **workshop** titled “**System design on Zynq Using SDSoC**” conducted by CoreEL Technologies(I) Pvt. Ltd and Indian Institute Technology, Madras held on 2nd & 3rd January 2016.
12. Attended two days National Seminar titled “**IPR Awareness & Research Methodologies**” held on 27th & 28th May 2016 at Karpaga Vinayaga College of Engineering & Technology.
13. Attended UKIERI sponsored National level three days **workshop** on “**Advanced Topics in Signal Processing , RF & Wireless Communication**”, held on 13th-15th June 2016 at IIITD & M, Kancheepuram.
14. Attended five days **FDP** titled “**VLSI Design**” held on 25th - 29th November 2013 at Dhanalakshmi College of Engineering.
15. Attended seven days **FDP** for the subject titled “**EC2354-VLSI Design**” held from 8th - 14th November 2014 at Karpaga Vinayaga College of Engineering & Technology.
16. Attended Faculty Enablement Program on “**A step to outcome based Accreditation**”, held on 4th April 2015 at Sri Sairam Insititute of Technology.
17. Attended one week **FUP** on “**Digital system design using Verilog HDL**” Organized by C-DAC Hyderabad, held from 5th may -9th May 2015.
18. Attended ISTE sponsored **FDP** on “**EMF theory and transmission lines**”, held on 25th and 26th June 2015 at Sri Sairam Insititute of Technology.
19. Attended two days **FDP** on “**Modern teaching and learning methodologies for competency development** “organized by department of ECE , held on 3rd and 4th May 2017 at Jeppiar Maamallan Engineering College.
20. Attended two days **FDP** on “**Time management** ”organized by ITC Academy , held on 8th and 9th May 2017 at SA Engineering College.
21. Attended 6 days **FDP** on “**RF and Microwave and circuit design using Ansys Electro magnetic suite** ”held on 25th to 30th October 2017 at Sathyabama University.

